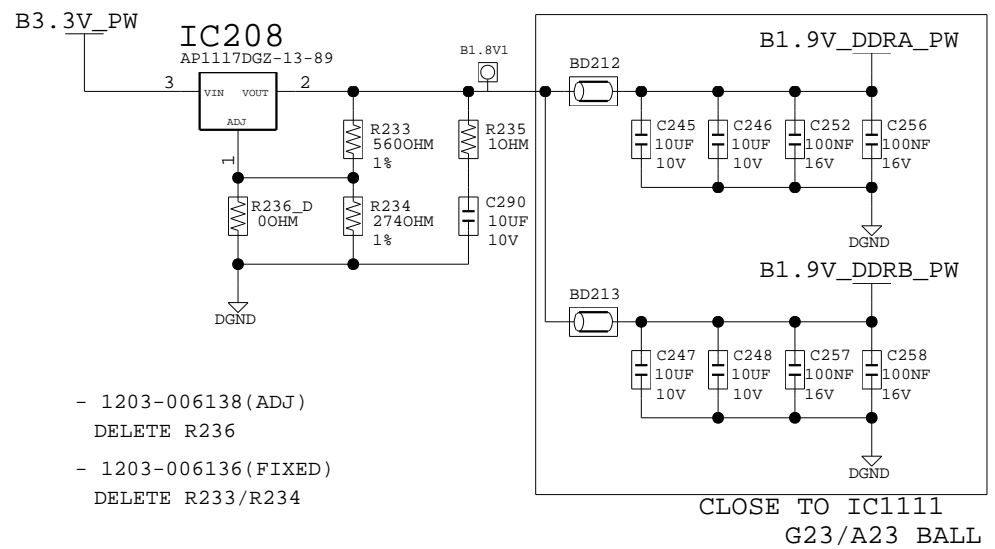


	1	2	3	4	5	6	7	8	
A	<div>SAMSUNG PROPRIETARY</div> <div>THIS DOCUMENT CONTAINS CONFIDENTIAL PROPRIETARY INFORMATION THAT IS SAMSUNG ELECTRONIS CO'S PROPRIETARY. DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS EXCEPT AS AUTHORIZED BY SAMSUNG.</div> <div>Comply with the threshold of substances which are specified in 0QA-2049</div> <div><div>BLOCK NAME</div><div>x4_dvb_h_lcd4_d2</div></div> <div><div>BLK PAGE NO.</div><div>1 OF 15</div></div>		<div>X4_DVB</div> <div>( HALF NIM )</div>						A
B	<div>- . X4_DVB_H_LCD4_D2</div> <div>- . 2010/05/28</div>		<div>SCHEMATICS INDEX</div> <div>01_COVER_SHEET</div> <div>02_STANDBY / MAIN POWER</div> <div>03_POWER / GND / FUNCTION</div> <div>04_SUB MICOM</div> <div>05_X4_GPIO</div> <div>06_X4_VIDEO_AUDIO_HDMI_LVDS</div> <div>07_DDR2 MEMORY</div> <div>08_ONENAND / TS</div> <div>09_PCMCIA</div> <div>10_TUNER1</div> <div>11_SCART</div> <div>12_COMP / AV / PC</div> <div>13_HDMI</div> <div>14_SOUND</div> <div>15_LVDS / USB</div>						B
C	<div>&lt;LOCATION SUFFIX&gt;</div> <div><div><div><div>_FHD : FHD OUT</div><div>_HD : HD OUT</div></div><div><div>_EU : EUROPE</div><div>_AU : AUSTRALIA</div></div><div><div>_TC : TC TUNER</div></div><div><div>_ANA : ANA DIMMING ( `09 22" )</div><div>_PWM : PWM DIMMING</div></div><div><div>_LCD : LCD</div><div>_PDP : PDP</div></div><div><div>_F : SOUND FILTER</div><div>_FL : SOUND FILTERLESS ( 19/22" )</div></div><div><div>_JEIDA : LVDS FORMAT JEIDA</div></div><div><div>_IR3 : USE 3.3V FOR FUNCTION</div><div>_IR5 : USE 5V FOR FUNCTION</div></div><div><div>_D : DELETE</div></div><div><div>_60HZ / _120HZ : 60HZ/120HZ OUT</div></div></div></div>		<div>PCB STACKUP</div> <div>LAYER 1 ----- TOP ( SIGNAL1 )</div> <div>LAYER 2 ----- INNER 1 ( GND )</div> <div>LAYER 3 ----- INNER 2 ( SIGNAL2 &amp; POWER )</div> <div>LAYER 4 ----- BOTTOM ( SIGNAL3 )</div>						C
D									D
E			<div>MODEL : X4_DVB_H_LCD4_D2</div> <div>CODE NO : BN41-01538A</div> <div>REV : ( 2010.05.28 )</div> <div>SIZE : 157X122 1.2T</div>						E
F			<div><div><div>PROJECT NAME</div><div>x4_dvb_h_lcd4_d2</div></div><div><div>A3</div><div>DRAWN BY</div><div>Unknown</div></div><div><div><div>SAMSUNG</div><div>DEV.STEP</div><div>Unknown</div></div><div><div>REV.</div><div>1.0</div></div><div>APPROVED BY</div><div>Unknown</div></div><div><div>LAST EDITED TIME</div><div>Fri May 28 18:09:12 2010</div></div><div><div>TOTAL PAGE NO.</div><div>1 OF 15</div></div></div>						F
	1	2	3	4	5	6	7	8	

F

H:B	POWER	OFF
L:B	POWER	ON

F

A

B

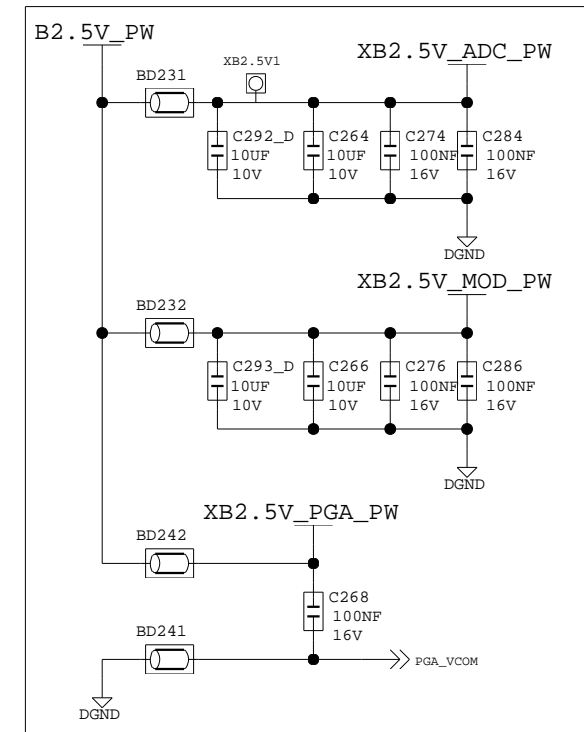
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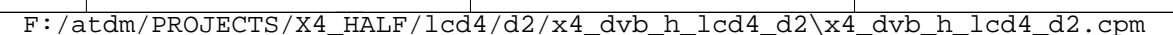
E

F

## X4 POWER & GND



# FUNCTION



04\_SUBMCOM

SUB <2KBIT>

SUB\_EEPROM\_WP

SUB\_EEPROM\_SCL

SUB\_EEPROM\_SDA

A3.3V\_PW

SUBEEP\_A3.3V

IC502  
AT24C02BN-SH-T

8

7

9

5

VCC

E0

E1

MODE, /WC

SCL

E2

SDA

VSS

1

2

3

4

DGND

C421

100NF

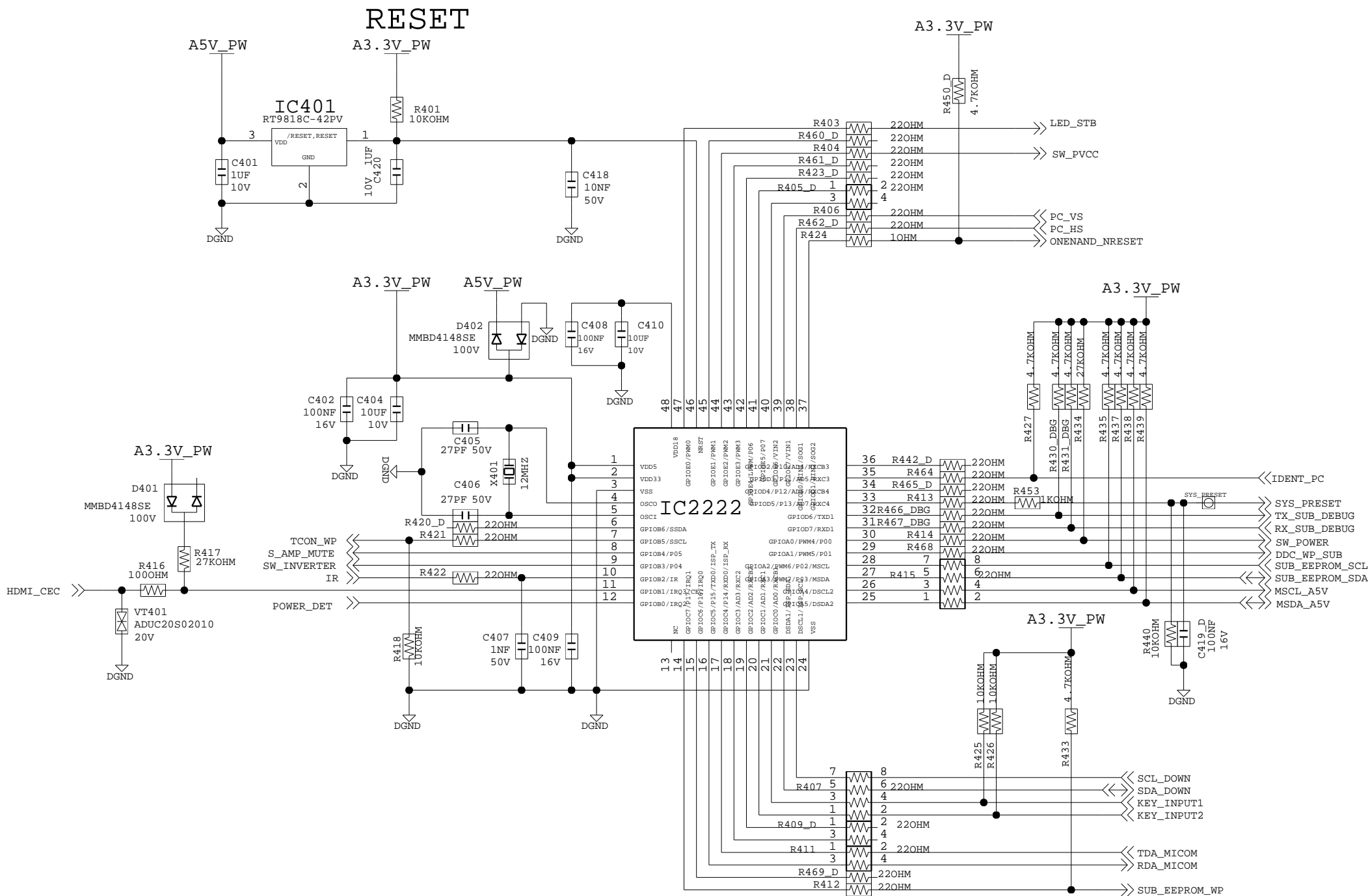
16V

DGND

SUBEEP\_SDA

SUBEEP\_SCL

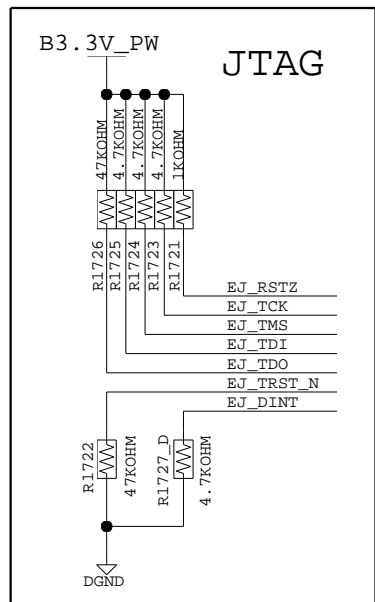
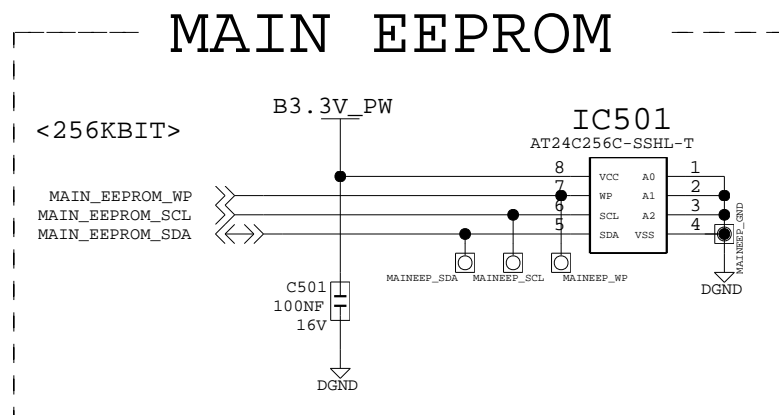
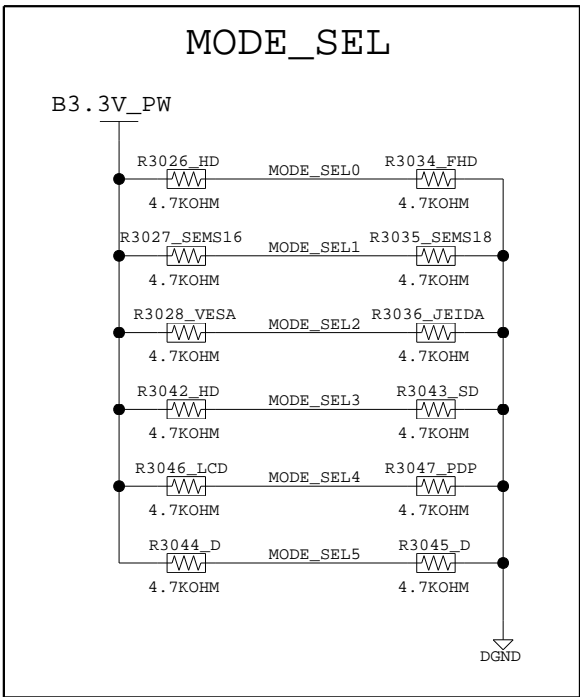
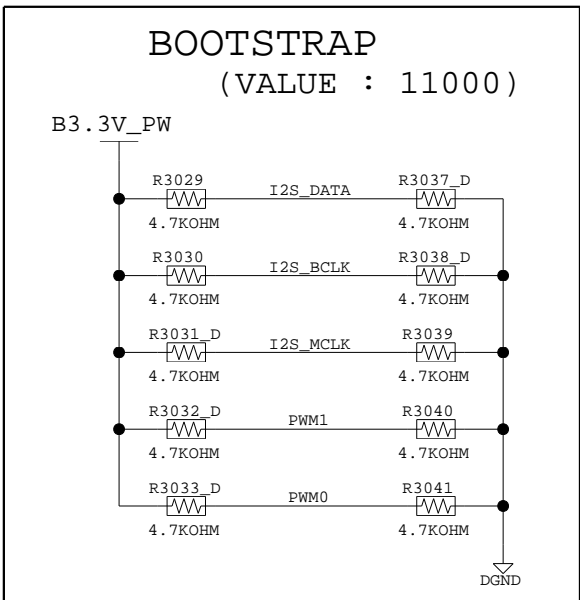
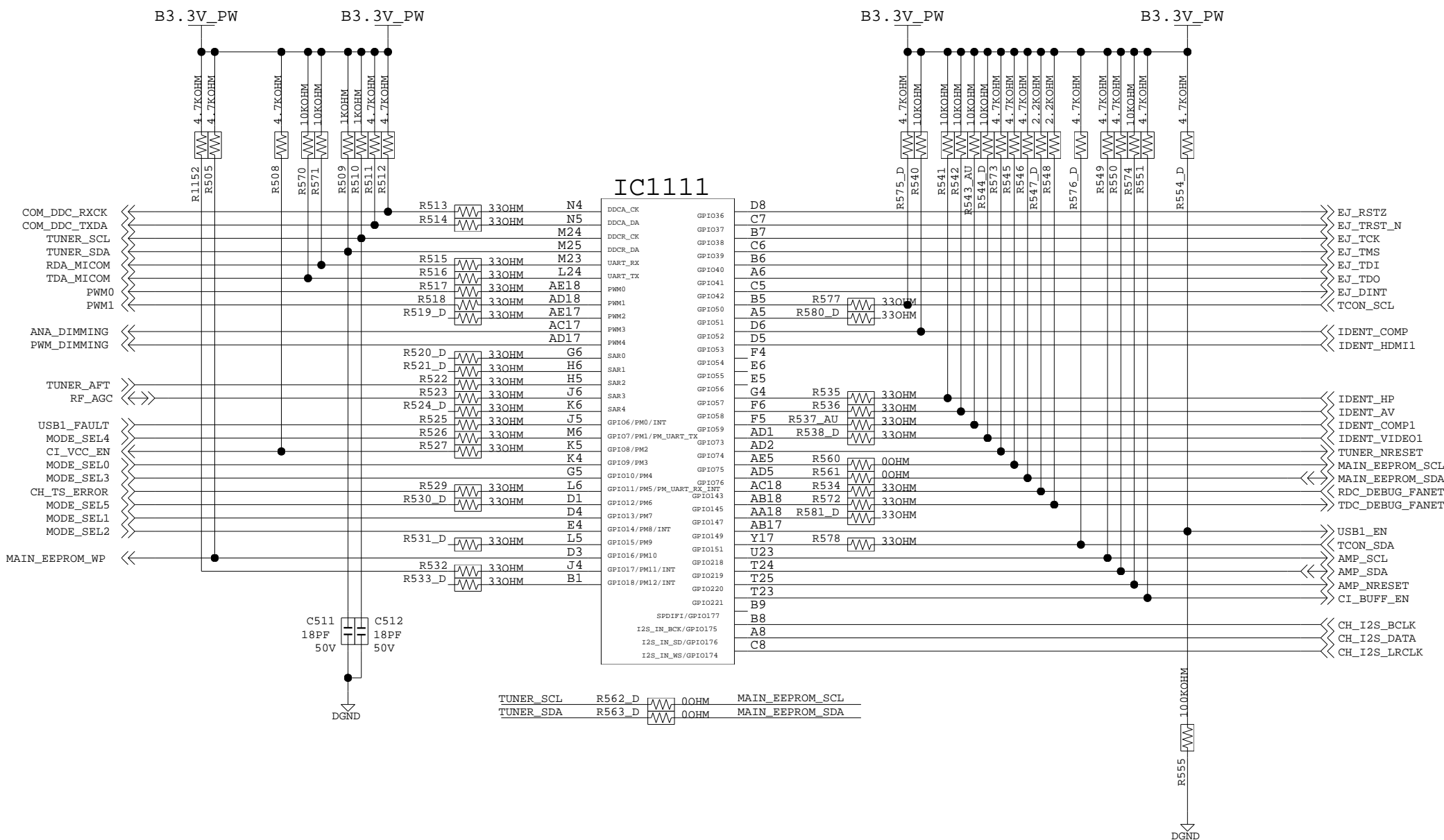
SUBEEP\_WP



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				Unknown	
<b>SAMSUNG</b>	DEV.STEP	REV.		APPROVED BY	
	Unknown	1.0		Unknown	
LAST EDITED TIME				TOTAL PAGE NO.	
Tue Jun 01 15:05:25 2010				4 OF 15	

# 05\_X4\_GPIO

BLOCK NAME x4_dvb_h_lcd4_d2	BLK PAGE NO. 5 OF
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			Unknown	
<b>SAMSUNG</b>	DEV.STEP	REV.	APPROVED BY	
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LAST EDITED TIME			TOTAL PAGE NO.	
Tue Jun 01 15:05:33 2010			5 OF 15	

A

B



## D



## F

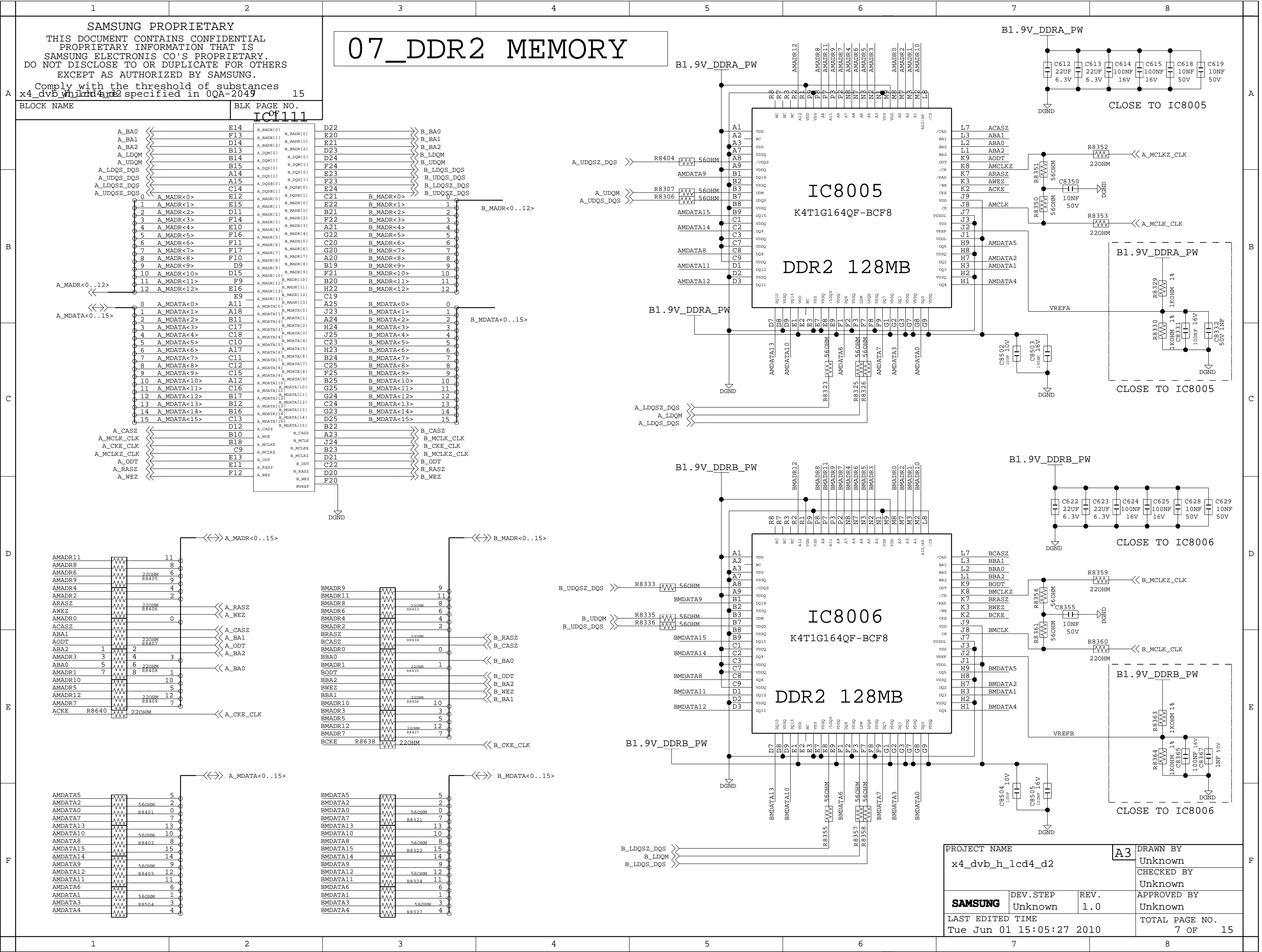
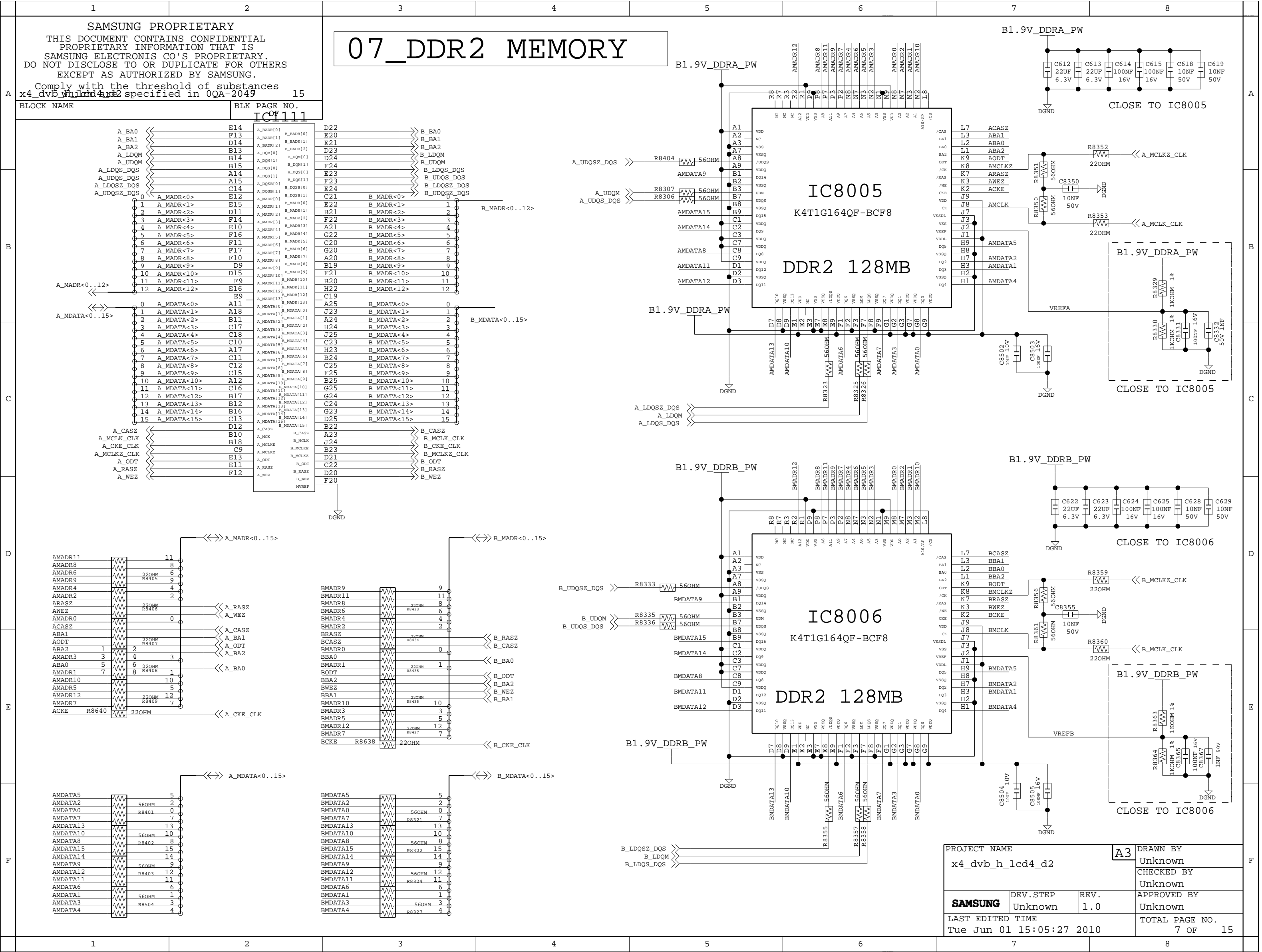


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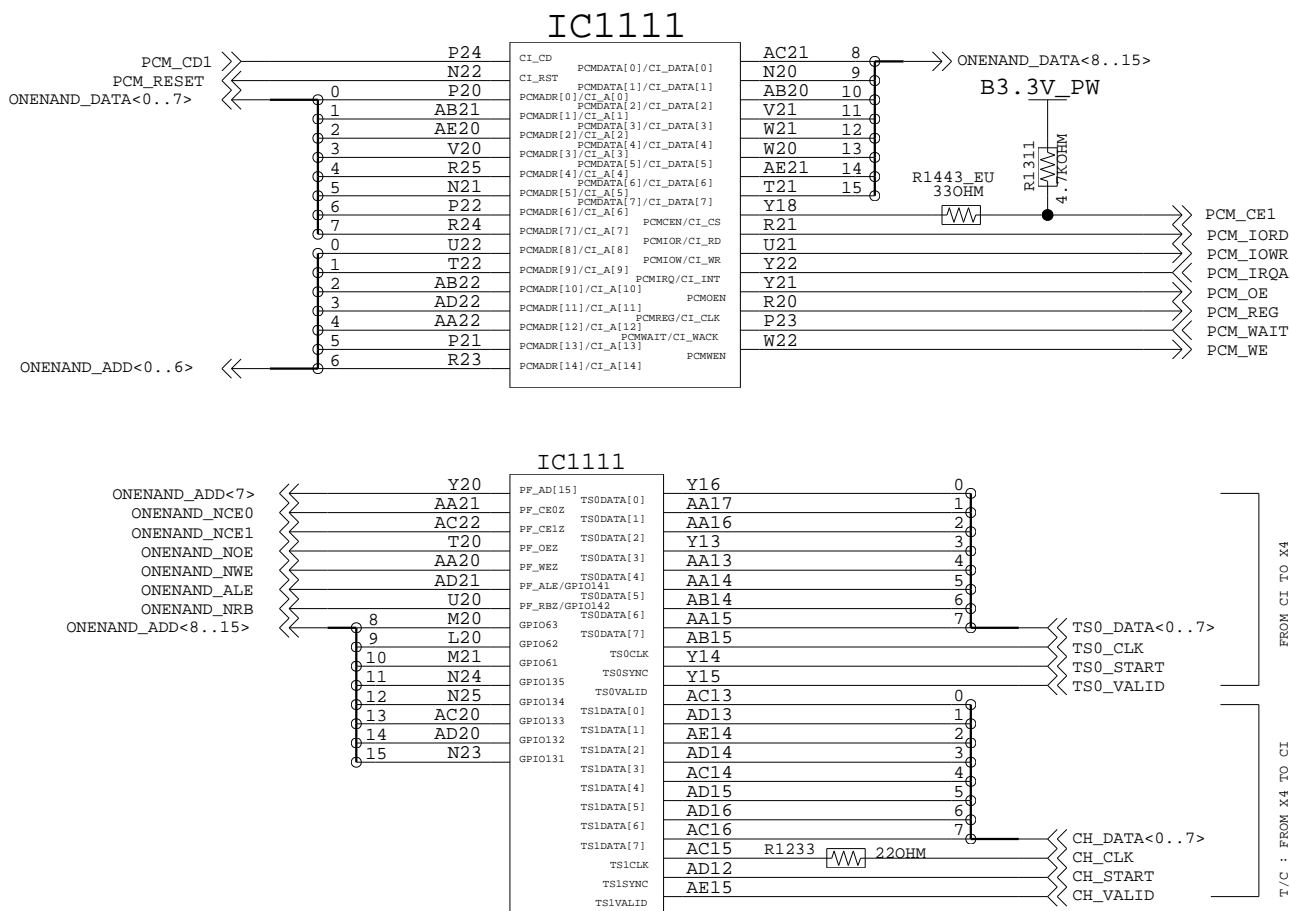
## B



F



08\_ONENAND/TS



T/C : FROM X4 TO CI  
S2 : FROM S-TUNER TO X4

T/C : FROM X4 TO CI  
S2 : FROM S-TUNER TO X4

A3	DRAWN BY Unknown
	CHECKED BY Unknown
V. 0	APPROVED BY Unknown
10	TOTAL PAGE NO. 8 OF 15



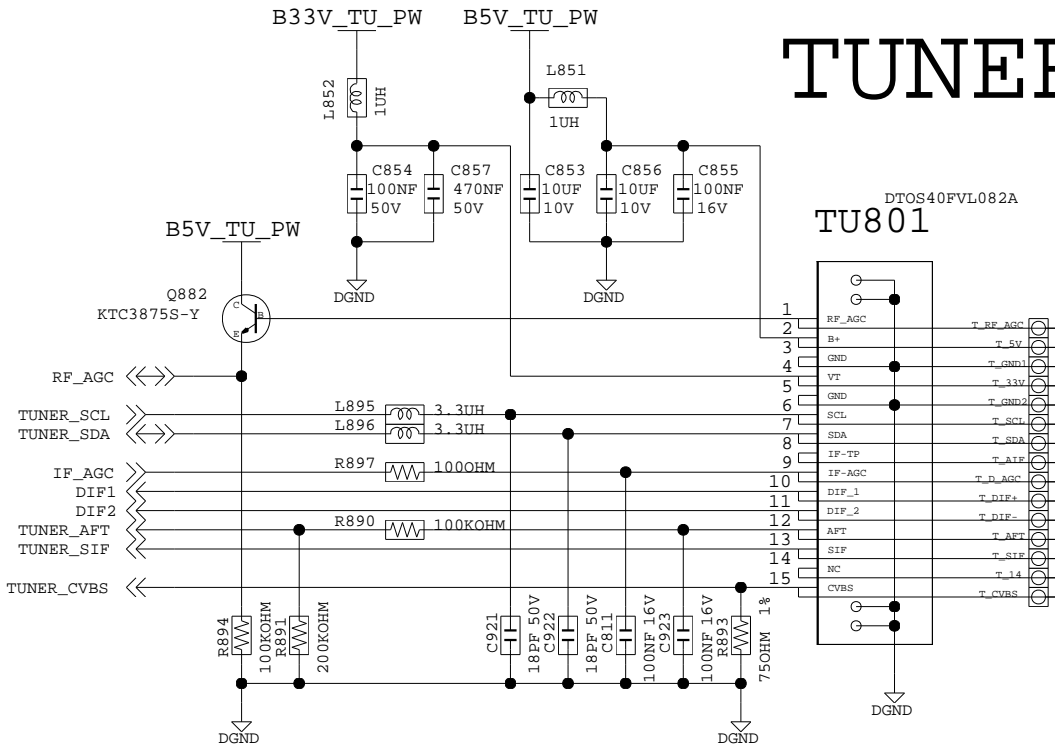


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10\_TUNER1

BLOCK NAME	BLK PAGE NO.
x4_dvb_h_1cd4_d2	10 OF 15

# TUNER



# CLOSE TO TUNER

IF+ << IF- <<

C818 1NF 50V C819 1NF 50V

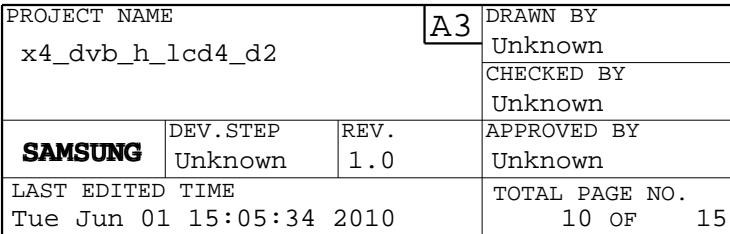
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L854 1uH L855 1uH

C821 2.7pF

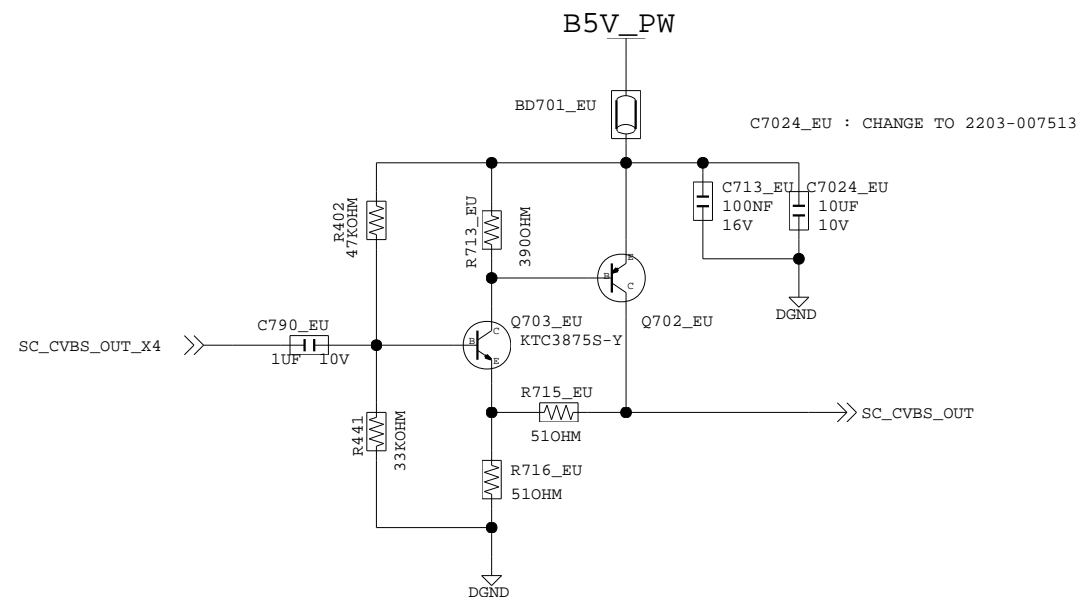
L804 100pF

DIF1 DIF2



# 11\_SCART

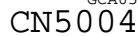
SCART



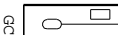
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				CHECKED BY	
				Unknown	
<b>SAMSUNG</b>	DEV.STEP	REV.		APPROVED BY	
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LAST EDITED TIME				TOTAL PAGE NO.	
Tue Jun 01 15:05:32 2010				11 OF 15	

F

# COMPONENT

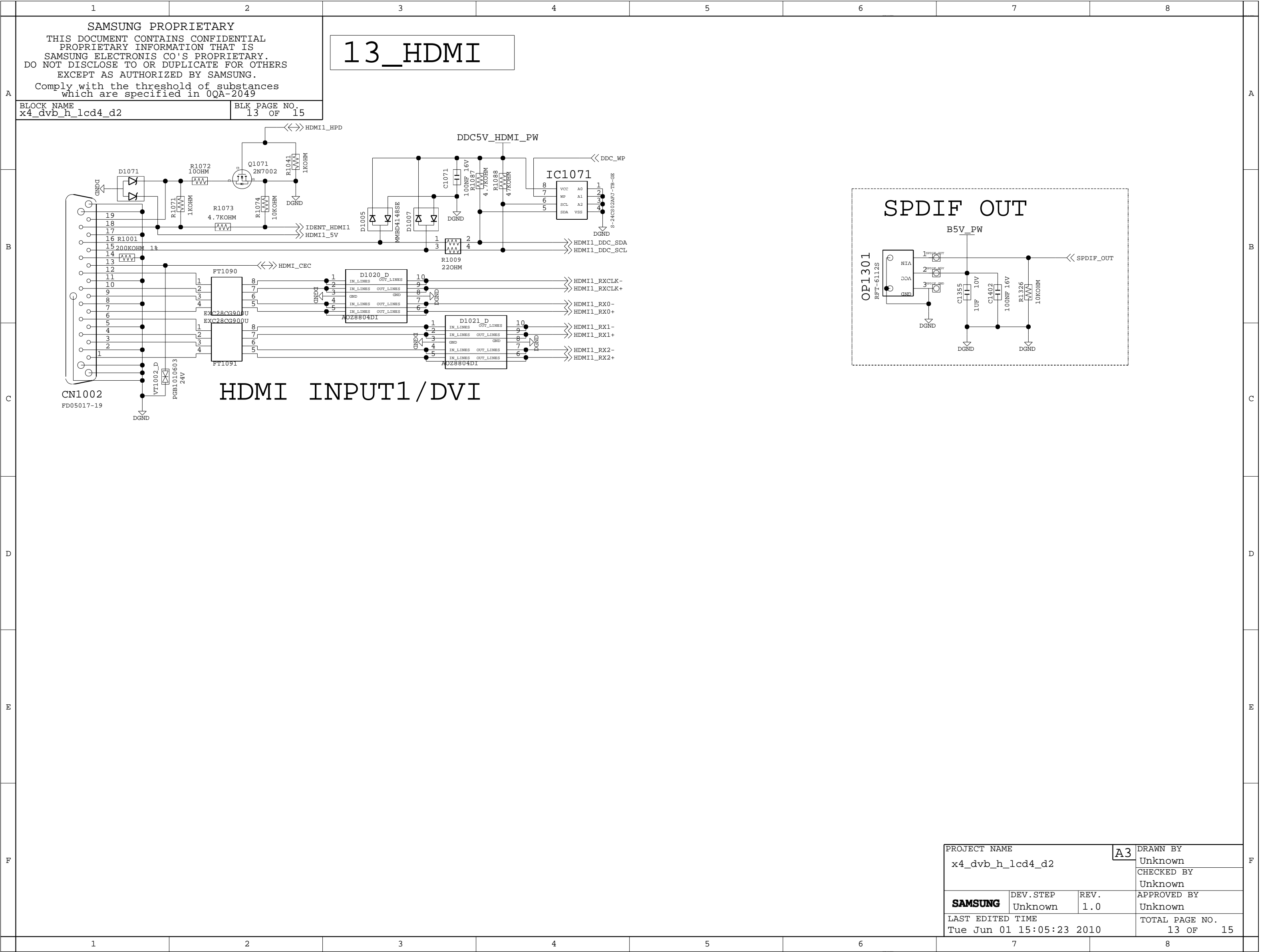


CN901



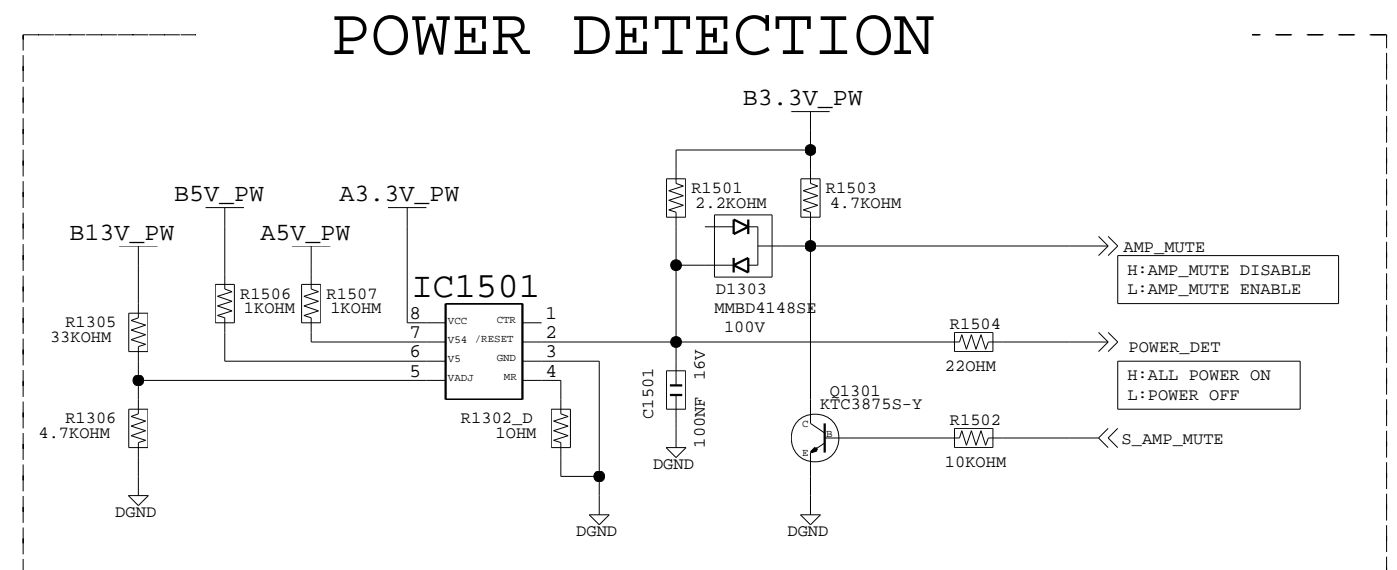
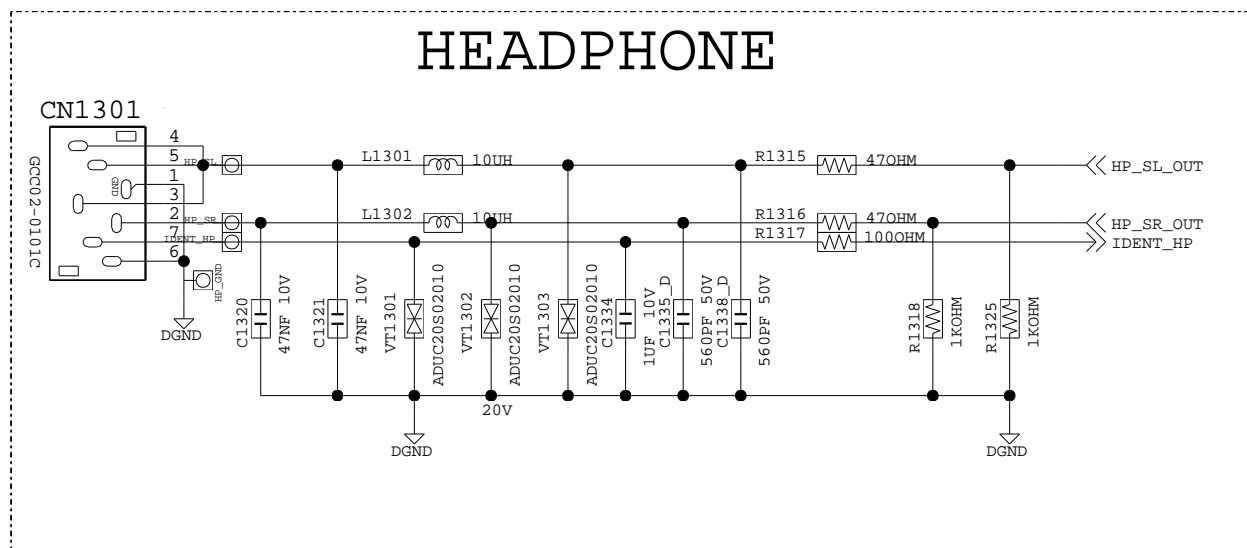
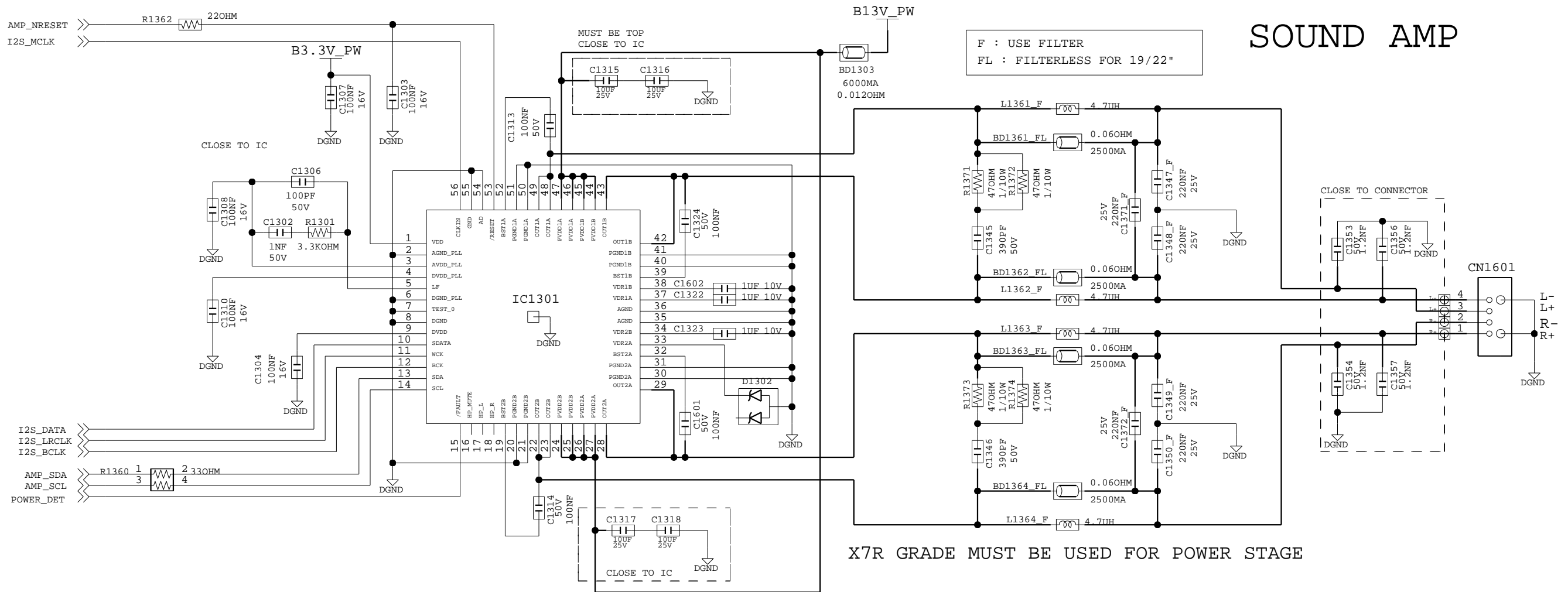
12\_COMP / AV / PC

[illegible][illegible]F



14\_SOUND

BLOCK NAME x4_dvb_h_lcd4_d2	BLK PAGE NO. 14 OF 15
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PROJECT NAME			A3	DRAWN BY	
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				CHECKED BY	
				Unknown	
SAMSUNG	DEV.STEP	REV.	APPROVED BY		
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LAST EDITED TIME			TOTAL PAGE NO.		
Tue Jun 01 15:05:24 2010			14 OF 15		

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